SUBJECT: MICROPROCESSOR ARCHITECTURE

AND PROGRAMMING

TOPIC: BUS ORGANIZATION OF 8085

NAME OF TEACHER: SIMNA V J

ACADEMIC YEAR: 2020-2021

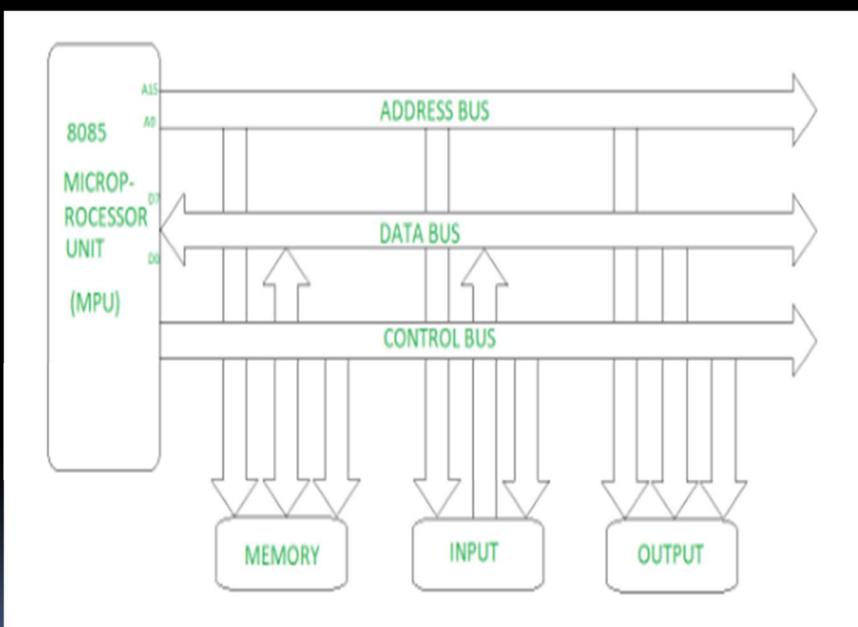
TOPIC:

BUS ORGANIZATION OF 8085

Bus organization of 8085 microprocessor

 Bus is a group of conducting wires which carries information.

 All the peripherals are connected to microprocessor through bus



Address bus

 It is a group of conducting wires which carries address only

 Address bus is unidirectional because data flow in one direction, from microprocessor to memory or I/o devices Length of address bus of 8085 is 16 bit (length determines amount of memory a system can address)

Data bus

It is a group of conducting wires which carries data only

Length of data bus is 8bits

 Data bus is bi directional.ie,data flow from microprocessor to memory or I/o devices and from memory or I/O device to microprocessor

Control bus

 It is a group of conducting wires, which is used to generate timing and control signals to control all the associated peripherals

- Some control signals are
- memory read
- Memory write
- ► I/o read
- > I/o write